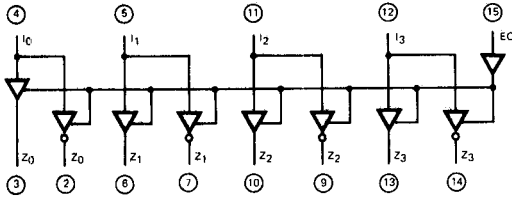


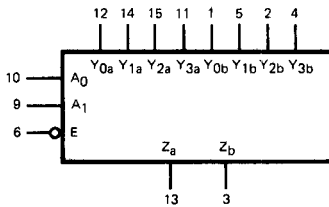
DIGITAL-CMOS

**C62  
4104B**



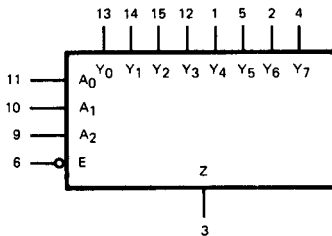
$V_{DD0}$  = Pin 1  
 $V_{DDI}$  = Pin 16  
 $V_{SS}$  = Pin 8

**C64  
4052B**



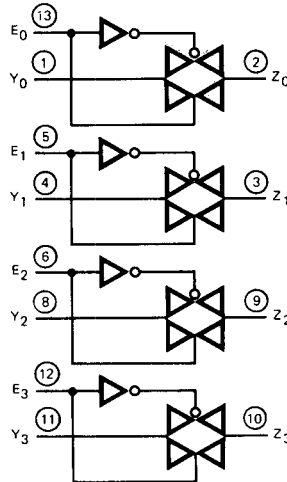
$V_{DD}$  = Pin 16  
 $V_{SS}$  = Pin 8  
 $V_{EE}$  = Pin 7

**C65  
4051B**



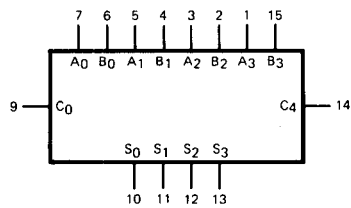
$V_{DD}$  = Pin 16  
 $V_{SS}$  = Pin 8  
 $V_{EE}$  = Pin 7

**C63  
4016B, 4066B**



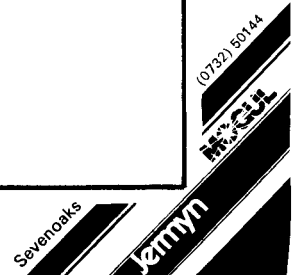
$V_{DD}$  = Pin 14  
 $V_{SS}$  = Pin 7

**C66  
4008B**



$V_{DD}$  = Pin 16  
 $V_{SS}$  = Pin 8

NOTE: The Flatpak versions have the same pinouts (Connection Diagram) as the Dual In-Line Packages.



## FAIRCHILD INTERFACE

### SWITCHES

#### ANALOG SWITCHES

Item	DEVICE NO.	Description	Input Logic	Channel Resistance $\Omega$ (Max)	Supply Voltage V	Logic/Connection Diagram	Packages
1	SH3002	SPDT Analog Switch	TTL	200	$\pm 12$	H6	TO-100
2	SH3003	DPST Analog Switch	TTL	200	$\pm 12$	H7	TO-100
3	4016B	Quad Bilateral SPST Switch	CMOS	1080	3.0 to 15	C63	3I,6A,9A
4	4051B	8-Chan Analog Multiplexer	CMOS	340	3.0 to 15	C65	4L,6B,9B
5	4052B	Dual 4-Chan Analog Multiplexer	CMOS	340	3.0 to 15	C64	4L,6B,9B
6	4053B	Triple 2-Chan Analog Multiplexer	CMOS	340	3.0 to 15	C96	4L,6B,9B
7	4066B	Quad Bilateral SPST Switch	CMOS	300	3.0 to 15	C63	3I,6A,9A
8	4067B	16-Chan Analog Multiplexer	CMOS	340	3.0 to 15	C97	4M,6N,9N
9	4741B	4x4 Crosspoint Switch	CMOS	340	3.0 to 15	C98	4L,6B,9B

### SPECIAL FUNCTIONS

#### TIMERS AND COUNTERS

Item	DEVICE NO.*	Function	Time Delay Hrs	Free Running Frequency KHz	Output Compatibility	Output Current mA	Supply Voltage V (Max)	Timing Error %	Logic/Connection Diagram	Package(s)
10	$\mu$ A555	Single Timer	1.0	100	TTL	200	18	1.0	I28	5B,9T
11	$\mu$ A556	Dual Timer	1.0	100	TTL	200	18	1.0	I29	7B,9B
12	$\mu$ A2240	Programmable Timer-Counter	120	—	TTL	5.0	18	0.5	I27	7B,9B

\*In some cases, only commercial temperature range devices are given. Please request specific information for military versions.

**FAIRCHILD DIGITAL**

**CMOS**

**REGISTERS**

Item	Function	DEVICE NO.	No. of Bits	Serial Entry	Parallel Entry No. of Bits	Clock Edge	Max Clock Frequency MHz (Typ) VDD = 10V	Clock To Output Delay - ns (Typ) VDD = 10V	Logic/Connection Diagram	Package(s)
1	Parallel-In/Parallel-Out	4035B	4	J $\bar{K}$	4	L-H	17	90	C39	4L,6B,9B
2	Parallel-In/Parallel-Out Bidirectional	40194B	4	D	4	L-H	14	45	C40	4L,6B,9B
3	Parallel-In/Parallel-Out	40195B	4	J $\bar{K}$	4	L-H	14	45	C41	4L,6B,9B
4	Serial-In/Parallel-Out	4015B	8	D	—	L-H	14	85	C42	4L,6B,9B
5	Parallel-In/Serial-Out	4014B	8	D	8	L-H	14.7	68	C43	4L,6B,9B
6	Parallel-In/Serial-Out	4021B	8	D	8	L-H	18.1	74	C44	4L,6B,9B
7	Serial-In/Serial-Out	4006B	18	D	—	H-L	30	37	C45	3I,6A,9A
8	Serial-In/Serial-Out	4731B	256	D	—	H-L	8.0	95	C46	3I,6A,9A
9	Serial-In/Serial-Out	4031B	64	D	—	L-H	8.0	60	C78	4L,6B,9B
10	Serial-In/Serial-Out Variable	4557B	1 to 64	D	—	2-H-L or L-H	10	150	C80	4L,6B,9B
11	Parallel/Serial-Input/Output	4034B	8	D	8	L-H	8.0	155	C79	4M,6N,9N

**DECODERS/DEMULTIPLIXERS**

Item	Function	DEVICE NO.	Address Inputs	Active LOW Enable	Output Configuration	Select Delay ns (Typ) VDD = 10V	Enable Delay ns (Typ) VDD = 10V	Logic/Connection Diagram	Package(s)
12	Dual 1-of-4 Decoder	4555B	2x2	2	H	60	60	C34	4L,6B,9B
13	Dual 1-of-4 Decoder	4556B	2x2	2	L	68	58	C35	4L,6B,9B
14	1-of-10 Decoder	4028B	4	—	H	66	—	C36	4L,6B,9B
15	1-of-16 Decoder	4514B	4	1	H	95	95	C37	4M,6N,9N,9U
16	1-of-16 Decoder	4515B	4	1	L	95	95	C38	4M,6N,9N,9U
17	Dual 4-Channel Demultiplexer	4052B	2	1	H	125	105	C64	4L,6B,9B

FAIRCHILD DIGITAL

CMOS

MONOSTABLES

Item	Function	DEVICE NO.	Typical Pulse Width Variation (%) V <sub>DD</sub> = 15V	No. of Inputs		Resetable	Output (tpw)-ns V <sub>DD</sub> = 5.0V	Logic/Connection Diagram	Package(s)
				Positive	Negative				
1	Dual Retriggerable Resetable Monostable Multivibrator	4528B	±3%	1	1	X	300	C61	4L,6B,9B
2	Low Power Monostable/Astable Multivibrator	4047B	—	1	1	X	—	C115	3I,6A,9A
3	Dual Precision Monostable Multivibrator	4538B	±0.5%	1	1	X	200	C116	4L,6B,9B
4	Micro Power Phase Locked Loop	4046B	—	—	—	—	—	C117	4L,6B,9B

ANALOG DEVICES

Item	Function	DEVICE NO.	Enable Input	Max ON Resistance-Ω V <sub>DD</sub> = V <sub>I/S</sub> = 10V	Max OFF State Leakage Current-nA V <sub>DD</sub> = 10V	Signal Capability V	Logic/Connection Diagram	Package(s)
5	Quad Bilateral Switch	4016B	X	840	125	0-15 ±7.5	C63	3I,6A,9A
6	Quad Bilateral Switch	4066B	X	520	100	0-15 ±7.5	C63	3I,6A,9A
7	Dual 4-Channel Multiplex/Demultiplex	4052B	X	600	100	0-15 ±7.5	C64	4L,6B,9B
8	8-Channel Multiplexer/Demultiplexer	4051B	X	600	100	0-15 ±7.5	C65	4L,6B,9B
9	Triple 2-Channel Multiplex/Demultiplexer	4053B	X	600	100	0-15 ±7.5	C96	4L,6B,9B
10	16 Channel Mux/Demux	4067B	X	600	100	0-15 ±7.5	C97	4M,6Q,9U
11	4x4 Cross Point Switch	4741B	X	840	100	0-15 ±7.5	C98	4L,6B,9B

9